

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jin-Kyoung Jung, *et al.*

Examiner: Almo, Khareem E.

Serial No.: 10/799,783

Group Art Unit: 2816

Filing Date: March 12, 2004

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Title: INTERNAL VOLTAGE GENERATING CIRCUIT FOR SEMICONDUCTOR
DEVICE

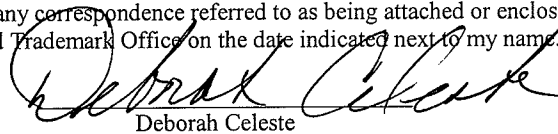
Customer No.: 29344

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CERTIFICATE OF ELECTRONIC SUBMISSION

I hereby certify that this correspondence and any correspondence referred to as being attached or enclosed is being sent by electronic submission to the United States Patent and Trademark Office on the date indicated next to my name.

October 31, 2007
Date


Deborah Celeste

Via Electronic Submission

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**RESPONSE FILED CONCURRENTLY WITH REQUEST FOR CONTINUED
EXAMINATION**

Sir:

The following is in response to the Advisory Action dated October 22, 2007, and is filed within the shortened statutory period of three months from the mailing of the final Office Action dated August 10, 2007, and is filed concurrently with a Request for Continued Examination (RCE).

A Listing of the Claims begins on page 2 of this paper.

Remarks/Arguments begin on page 15 of this paper.